

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
(Attorney Docket No. 11962R0US02U)

In the Application of:

Wen Tong et al.

§ Group Art Unit: 2662

§ Examiner: Hanh N. Nguyen

Serial No.: 09/766,267

§

Filed: January 19, 2001

§

For: FRAME STRUCTURE FOR
VARIABLE RATE WIRELESS
CHANNELS TRANSMITTING
HIGH SPEED DATA

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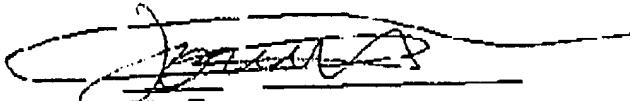
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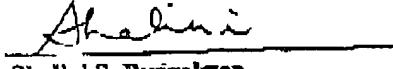
is attached hereto as Exhibit C.

I hereby state that all statements made herein are of my own knowledge, that they are true, and that all statements made herein are made on information and belief and are believed to be true. I acknowledge that any willful false statements made herein would be punishable by fine or imprisonment, or both (18 U.S.C. 1001) and may jeopardize the validity of the application or any patent issuing thereon.

Date: November 24, 2005



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AFFIDAVIT OF PRIOR INVENTION UNDER 37 C.F.R. 1.131 BY ASSIGNEE OF
INTEREST
(ON BEHALF OF DEPARTED INVENTOR LEO L. STRAWCZYNSKI)

My name is Curt Dodd. My home address is stated below by my signature. I am a patent agent and lawyer employed by Nortel Networks Limited. The facts stated herein are of my personal knowledge.

Wen Tong, Shalini S. Periyalwar, Leo L. Strawczynski, and Claude Royer are co-inventors of the above-referenced application. I am responsible for the continued prosecution of this application. This Affidavit is submitted by Nortel Networks Limited, the assignee of all right title and interest in this application and in lieu of submission of an affidavit by Leo L. Strawczynski, who is no longer is employed by Nortel Networks Limited and is unavailable to execute an affidavit. Attached hereto as Exhibit D is a Power of Attorney evidencing that I have the authority to execute this Affidavit on behalf of Nortel Networks Limited, the assignee of all right, title, and interest in this Application.

The invention of the present application was completed prior to January 7 of 2000. The invention was first disclosed internally on January 7, 2000. Attached hereto as Exhibit C is a copy of a disclosure document that was submitted internally on January 7, 2000, evidencing completion of this invention no later than January 7, 2000.

I hereby state that all statements made herein are of my own knowledge, that they are true, and that all statements made herein are made on information and belief and are believed to be true. I acknowledge that any willful false statements made herein would be punishable by fine or imprisonment, or both (18 U.S.C. 1001) and may jeopardize the validity of the application or any patent issuing thereon.

Date: November 25, 2005

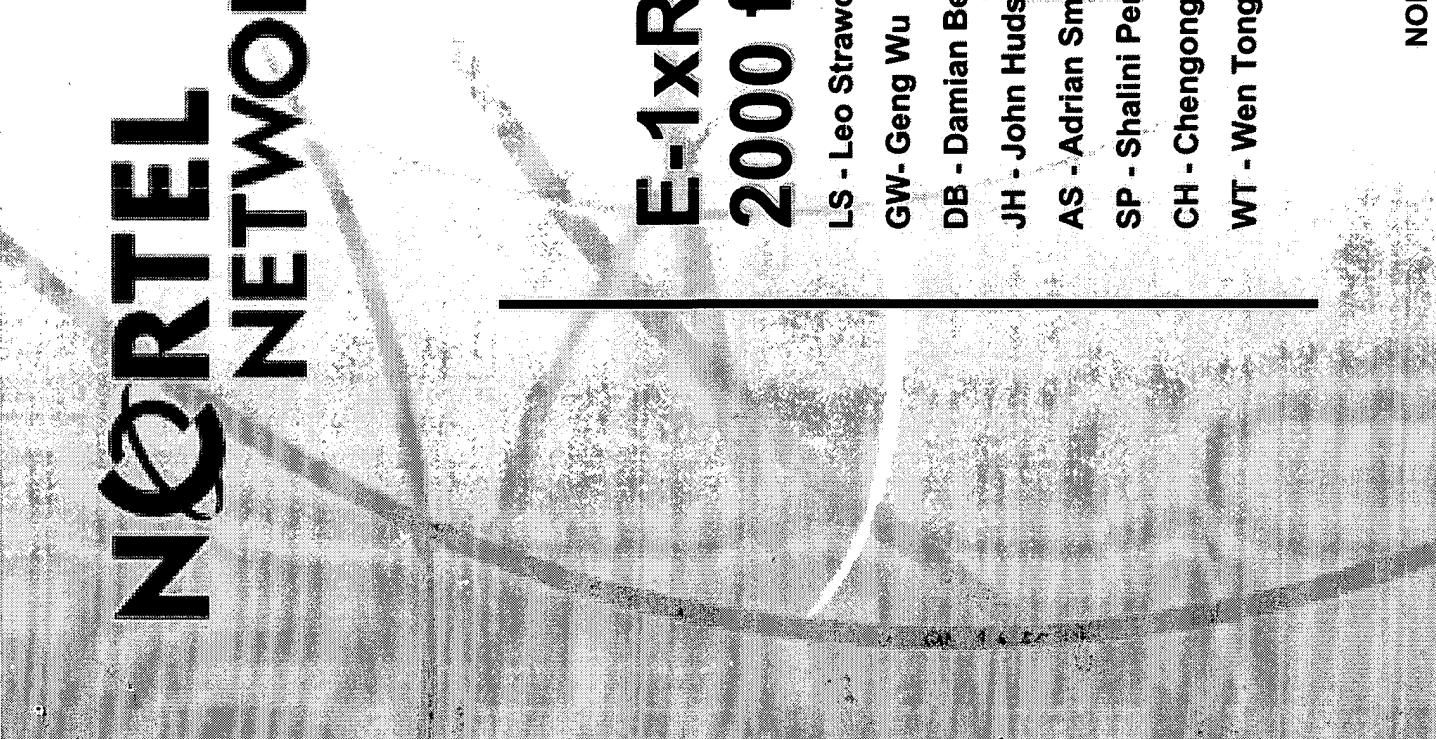

Curt Dodd

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E-1xRTT: The Evolution of IS- 2000 for High Speed Data

LS - Leo Strawczynski

GW - Geng Wu

DB - Damian Bevan

JH - John Hudson

AS - Adrian Smith

SP - Shalini Periwalayar

CH - Chengong Huang

WT - Wen Tong

Exhibit A

Physical Layer - Key System Aspects /Advantages of Physical Layer Design (WT)

Key Physical Layer Attributes	
Chip Rate	1.2288Mcps
FL Data Rates (Kbps)	76.8, 102.6, 153.6, 204.8, 307.2, 614.4, 921.6, 1228.8, 1843.2, 2457.6, 4915.2
RL Data Rates (Kbps)	9.6, 19.2, 38.4, 76.8, 153.6, 307.2
Modulation	FL: QPSK,8PSK,16/32QAM RL: BPSK
Coding	Turbo Coding (g=15/13, R=1/2)
RL Frame (ms)	2.5, 5, 20, 40, 80
FL Slot (ms)	1.25
Power Control	RL: Closed Loop (800Hz) FL:Non
Pilot	RL:CDM, FL: TDM
Multi-Code Covering	Mode-1: 16x16 Walsh Mode-2: Quasi-Orthogonal
Tx Diversity	Closed Loop:TxAA,Open Loop:STTD

- **RL Maximum Reuse 1XRTT**

- with minimum modifications

- **FL Fat TDM Pipe**

- slot based fast rate adaptation
- user C/I driven
- explicit rate information header

- **Highly Asymmetric Traffic Configuration**

Physical Layer - Key System Aspects /Advantages of Physical Layer Design (WT)

- **Higher Peak Data Rates and Spectral Efficiency**
 - 2.4Mbps, 16-QAM,
- **Advanced Adaptive Antenna Processing**
 - Hybrid TxAA/STTD at BTS, MRC at Terminal
- **Fast Adaptive Coding and Modulation**
 - Per User Rate and Modulation Adaptation Based on C/I
- **Allow Multi-User Detection (MUD) at Terminal**
 - Use Very Efficient MUD to Eliminate Error Floor of Rake Receiver



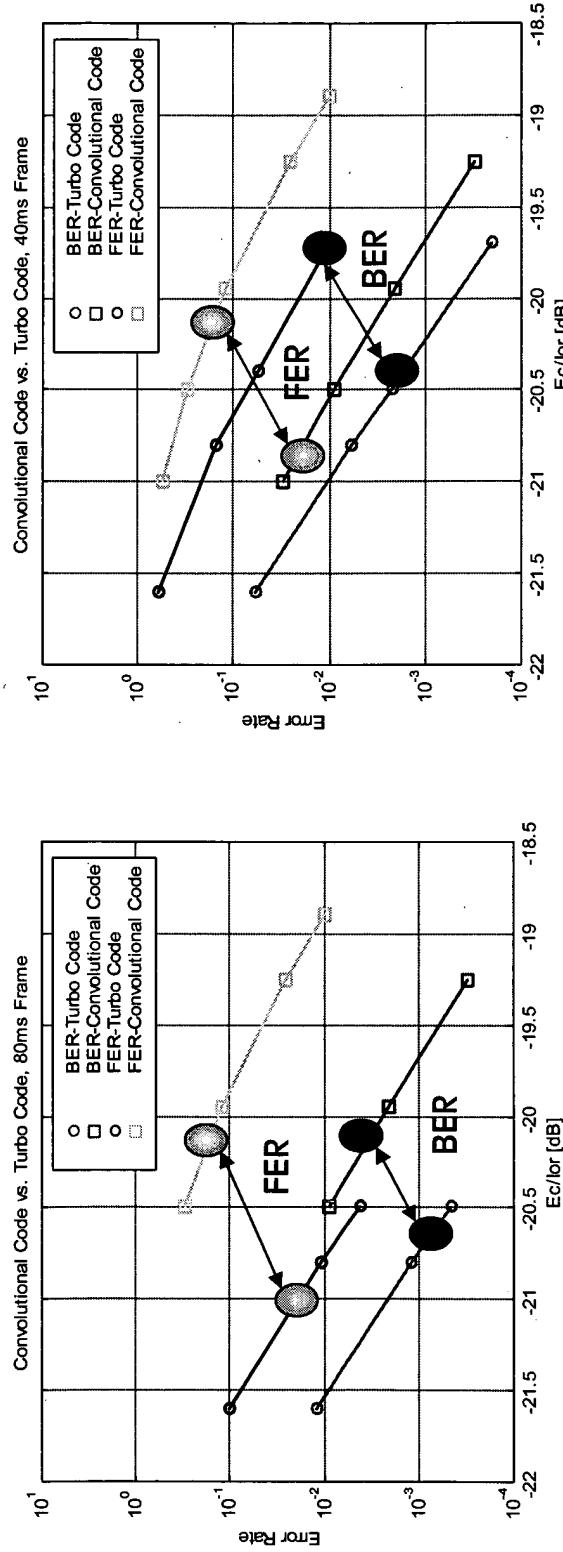
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Physical Layer - Key System Aspects /Advantages of Physical Layer Design (WT)

- **Soft Handoff at RL, Firm Handoff at FL**
 - Marco-Diversity MRC Combining SHO for RL
 - Buffered Packets with no Loss HO for FL
- **Adaptive Access Point Selection**
 - Terminal MRC Interference Rejection
- **Same RF Front-End as for 1XRTT**
 - For both BTS and Terminal

Reverse Link- Improvements/Modifications to 1xRTT (WT)

- Multi-Frame RL Fundamental Channel Structure
 - 40ms, 80ms frame for Radio Configuration 3 and 5
 - R=1/4 Turbo coding, 0.7-1.3dB gain, additional 70% Capacity gain
- Simulation Conditions:
 - Veh.-A Channel, 120Km/mph, Data Rate 9.6kbps, 4% Power Control Error Rate

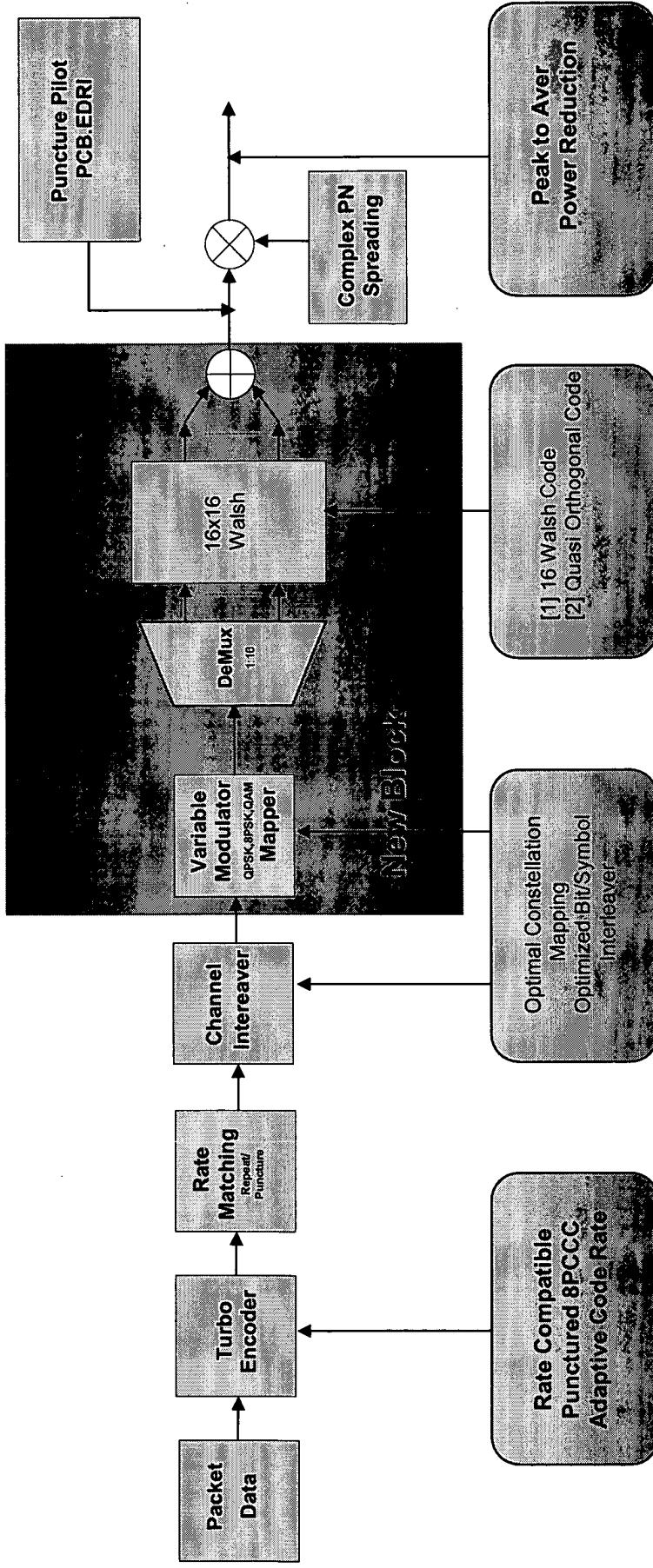


Reverse Link- Improvements/Modifications to 1xRTT (WT)

- New 2.5ms Frame Dedicated Control Channel for Fast In-Band Signaling
 - Use RDCCCH for average C/I report
 - To indicate preferred BTS for next frame transmission
 - To support alternate selection of TxAAC and STTD on the FL
- Reverse Power Control Sub-Channels
 - The power control sub-channel punctured on the reverse pilot channel is not can be used for fast signaling, i.e. delta value of C/I
- Enhanced Soft Handoff via Marco Diversity MRC at BSC

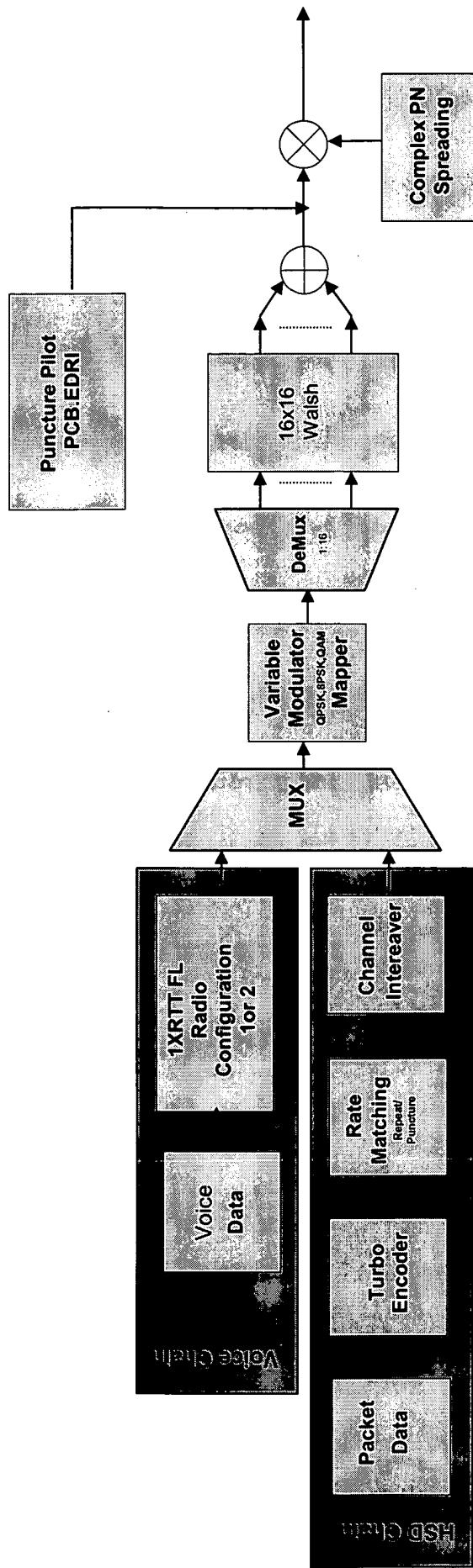
Forward Link (HSD Only Mode) (WT)

- Introduce Higher Order Modulations
 - To increase peak rates and throughput
- TDM Signaling Allows Higher Data Throughput



Forward Link (Simultaneous Voice and HSD)

- Introduce Sub-Frame Unit for Voice Insertion
 - Data Stealing from HSD Field
- Designed for 8 Simultaneous Voice and HSD Users
 - Supplementary to the HSD Users



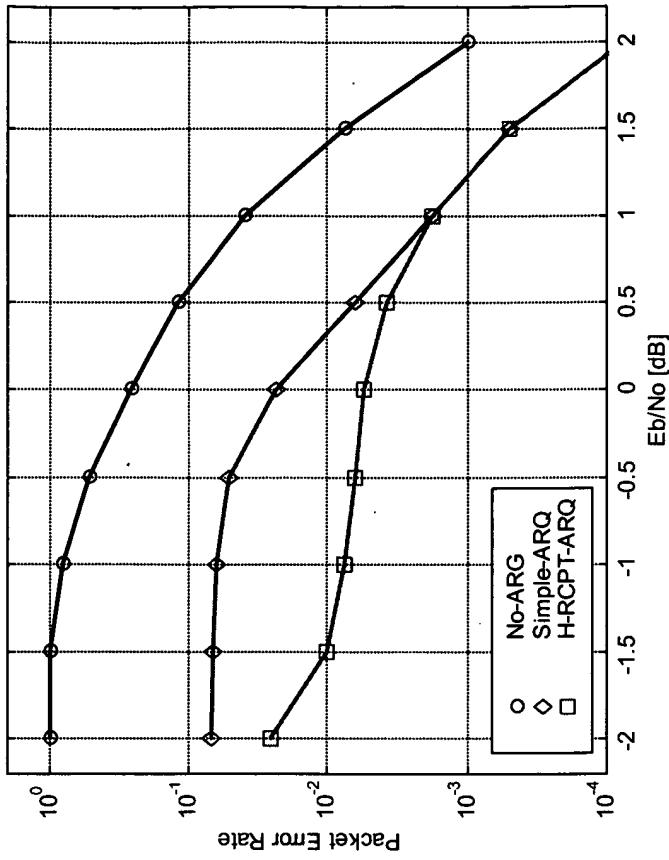
Adaptive Modulation and Coding (WT)

- **Short Term Block Fading Based Modulation Adaptation**
 - Date Rate Selection Per Slot Per User
- **Incremental Redundancy ARQ**
 - Rate Compatible Punctured Turbo Code (RCPTC)
- **Achieve Better Throughput by Partial Re-transmission**
- **Accommodate Different Error Protection**
 - Finer Granularity Rate Adjustment using Adaptive Hybrid ARQ
- **Rate Compatible Puncturing:**
 - Adjustable Coding Rates: $R=1, 2/3, 1/2, 2/5, 1/3$
 - Reduce the Sensitivity of Channel Estimation Error

Adaptive Modulation and Coding (WT)

- **Advantages of RCPTC-Hybrid ARQ**
 - Enhance throughput for low C/I users
 - Avoid fragmentation and re-assembly

Simulation Conditions	
Packet Size	2.5ms, 320 bits
FL Data Rate	153.6Kbps
Modulation	QPSK
Channel	2 GHz, AWGN
Coding	Turbo Coding ($g=15/13$, $R=1/3$)
RAKE Combining	Max. Ratio Combining, 4 fingers
Hybrid ARQ	Max. Retransmission = 3, Packet Combining,



Peak Data Rates (WT)

- FL & RL Peak Data Rates Follow 1XRTT Numerology
- Consider Simultaneously Voice and Data Service

HSD Forward Link Peak Rates

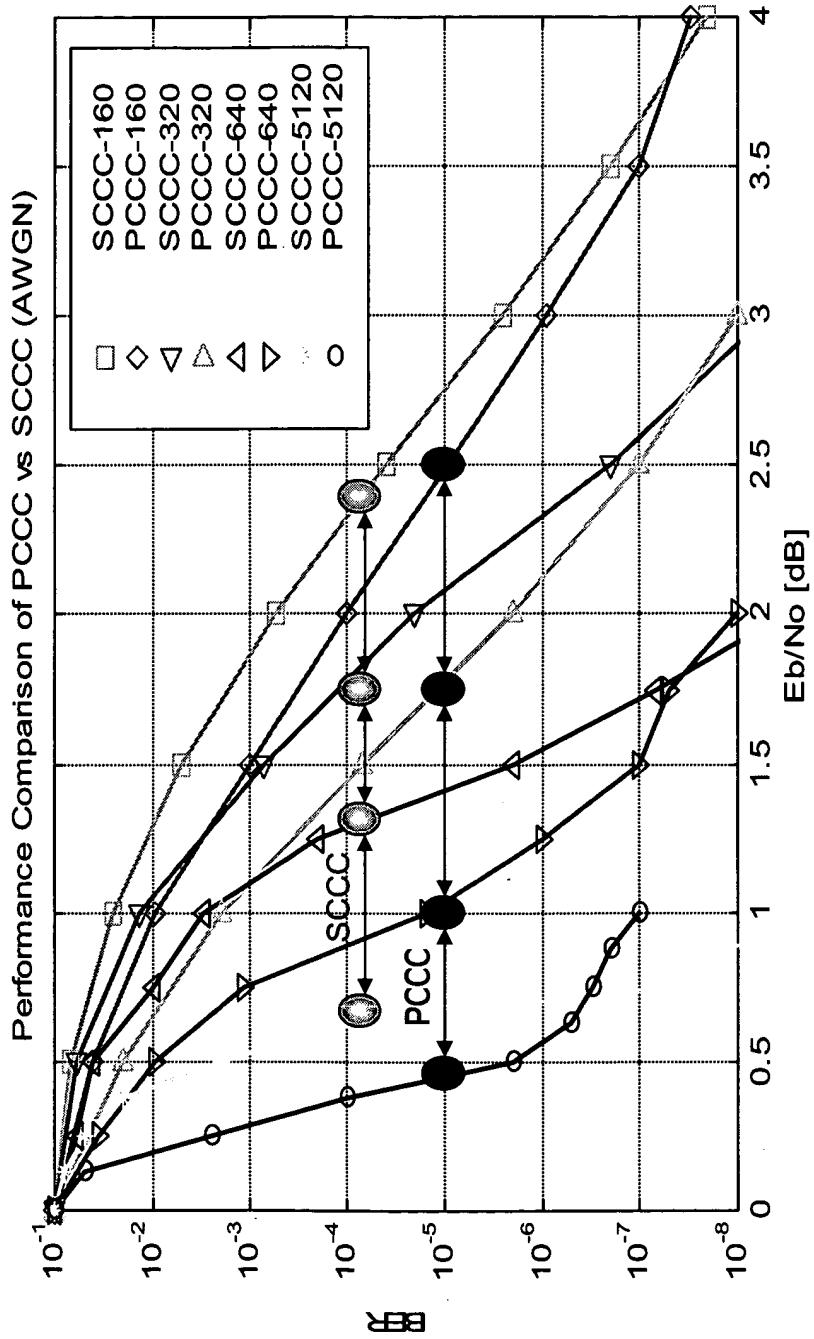
	Data rates (kbps)					
Parameter	76.8	153.6	307.2	614.4	921.6	1228.8
Bits/Per Packet	1536	1536	1536	1536	3072	2048
Modulation Type	QPSK				8 PSK	16 QAM

HSD Reverse Link Peak Rates

	Traffic				Access
Data Rate(kbps)	9.6	19.2	38.4	76.8	153.6
Bits per Packet	512	1024	2048	4096	8192
Modulation Type	BPSK				512

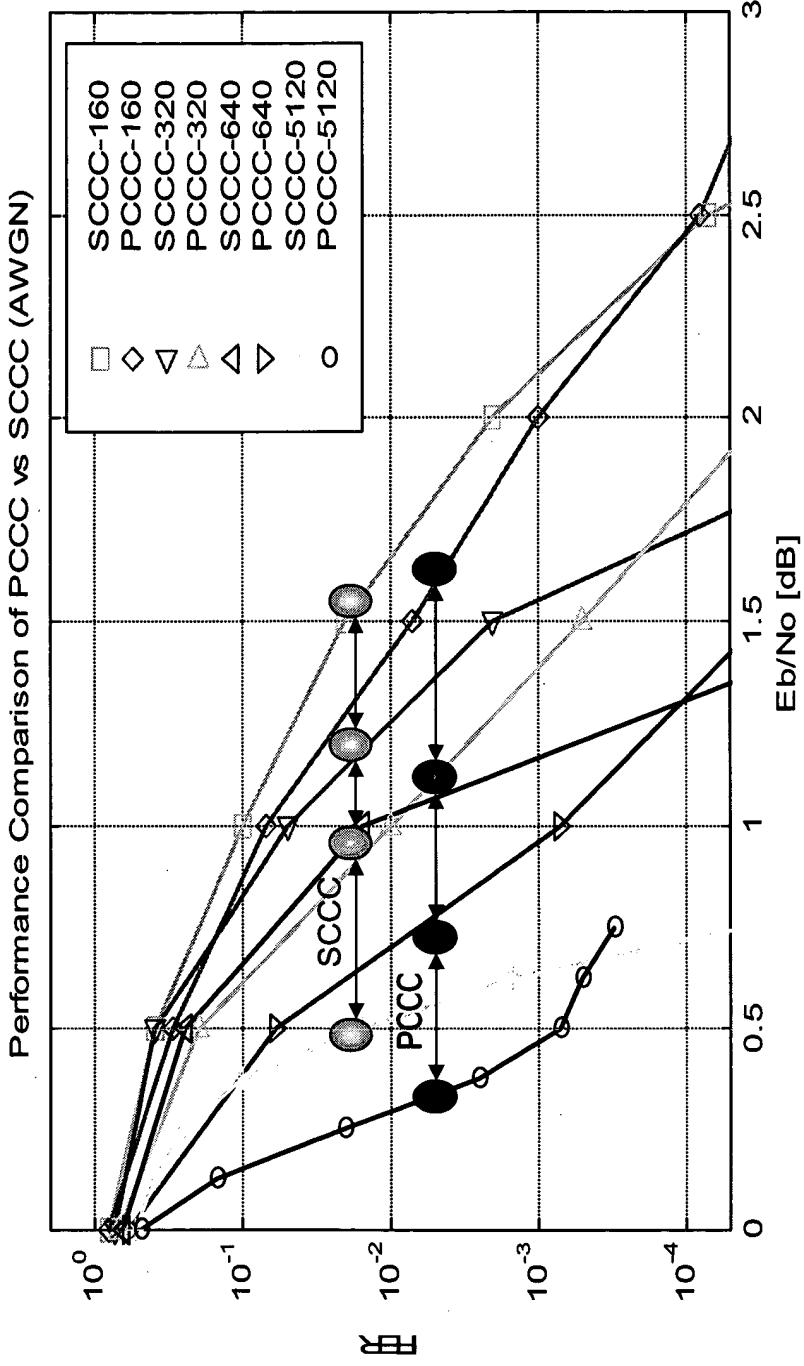
8-State PCCC Turbo Code(WT)

- 1XRTT 8-State Parallel Concatenated Convolutional Code(PCCC) has Better Performance than 4-State Serial Concatenated Convolutional Code (SCCC)



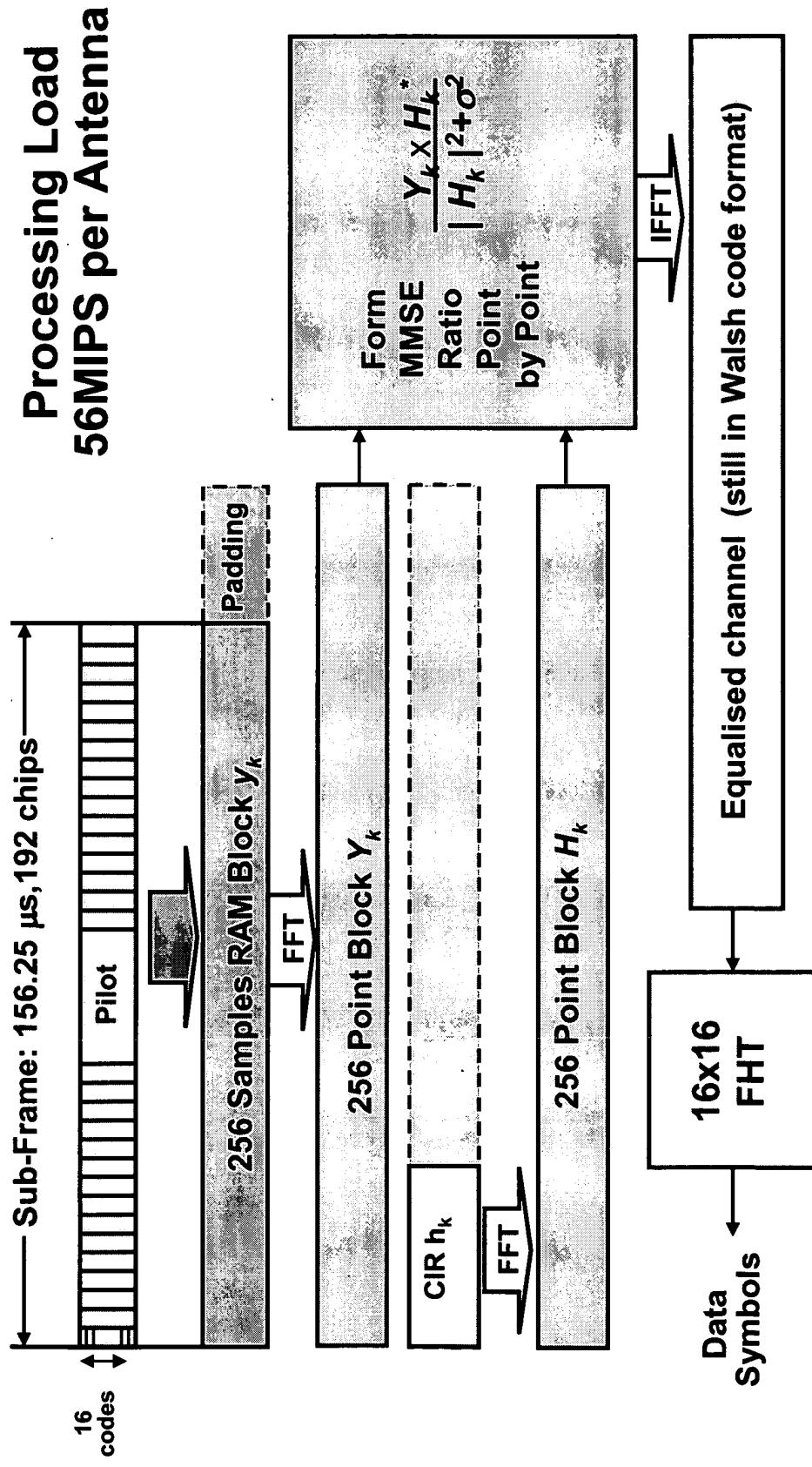
8-State PCCC Turbo Code(WT)

- The Turbo Decoders for 8-State PCCC and 4-State have same Complexity
 - PCCC has more gain over SCCC as packet size increases



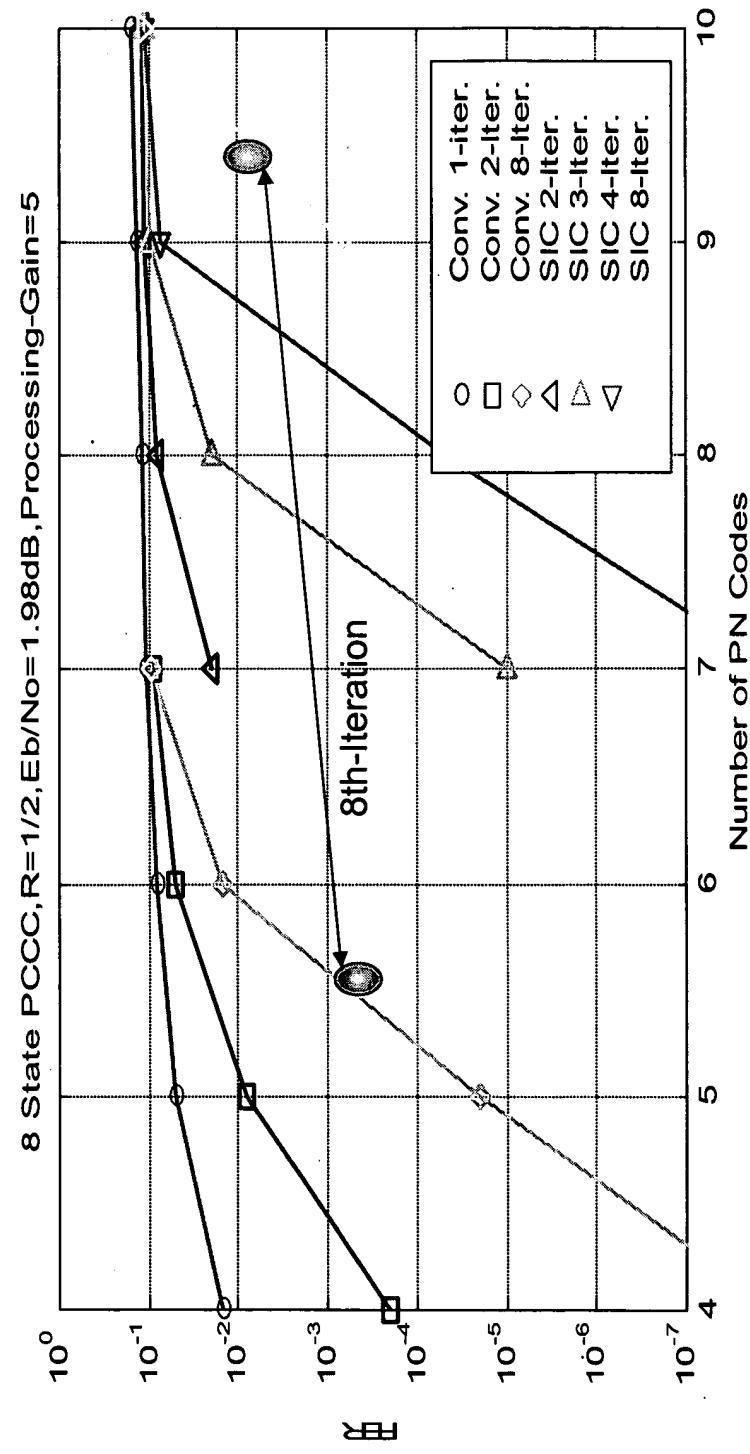
MUD Technique for Terminal

- High Efficient MUD for 16x16 Walsh Covering



MUD Technique for Terminal

- Using Quasi-Orthogonal Multi-Code Gain Spectral Efficiency
- Inter-Code Self Interference Cancellation at Terminal
 - Iterative Interference Cancellation Technique
 - 2-Stage MUD Gain 80% Spectral Efficiency



Terminal Receiver Structures (JH)



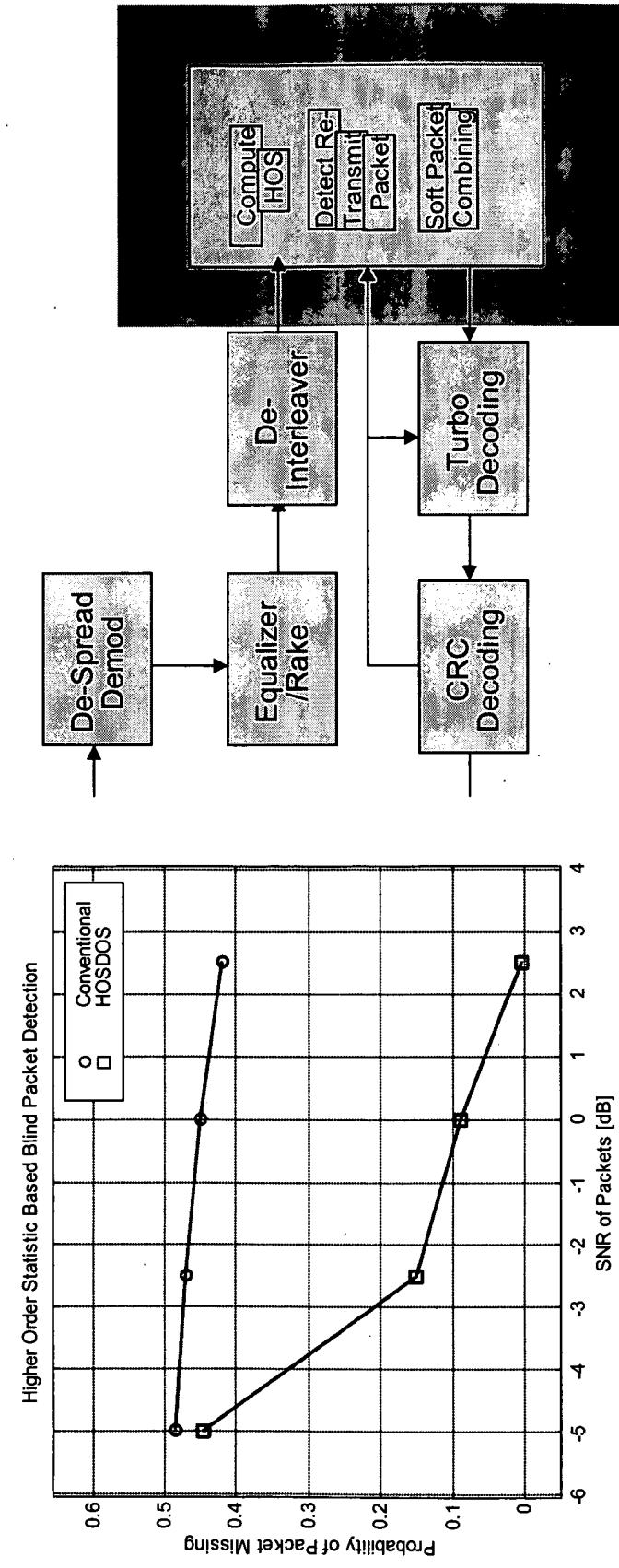
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ARQ with Soft-Combining & HOS (WT)

- **Blind Recursive Packet Combining**
 - Higher-Order-Statistic (HOS) Based Packet Detection
- **Allow Free Scheduling of Re-transmission of FL Packets**
 - No need for indication of re-transmission packet location
 - Reduce the FL protocol overhead
- **Lower Required SNR for Packet Transmission**
 - minimize power for the transmission of packets
 - minimize re-transmissions (to increase throughput, reduce delays)
- **Range Extension of Higher Data Rate Service**
 - Cell border high rate enhancement
 - Better to keep higher rate for re-transmission

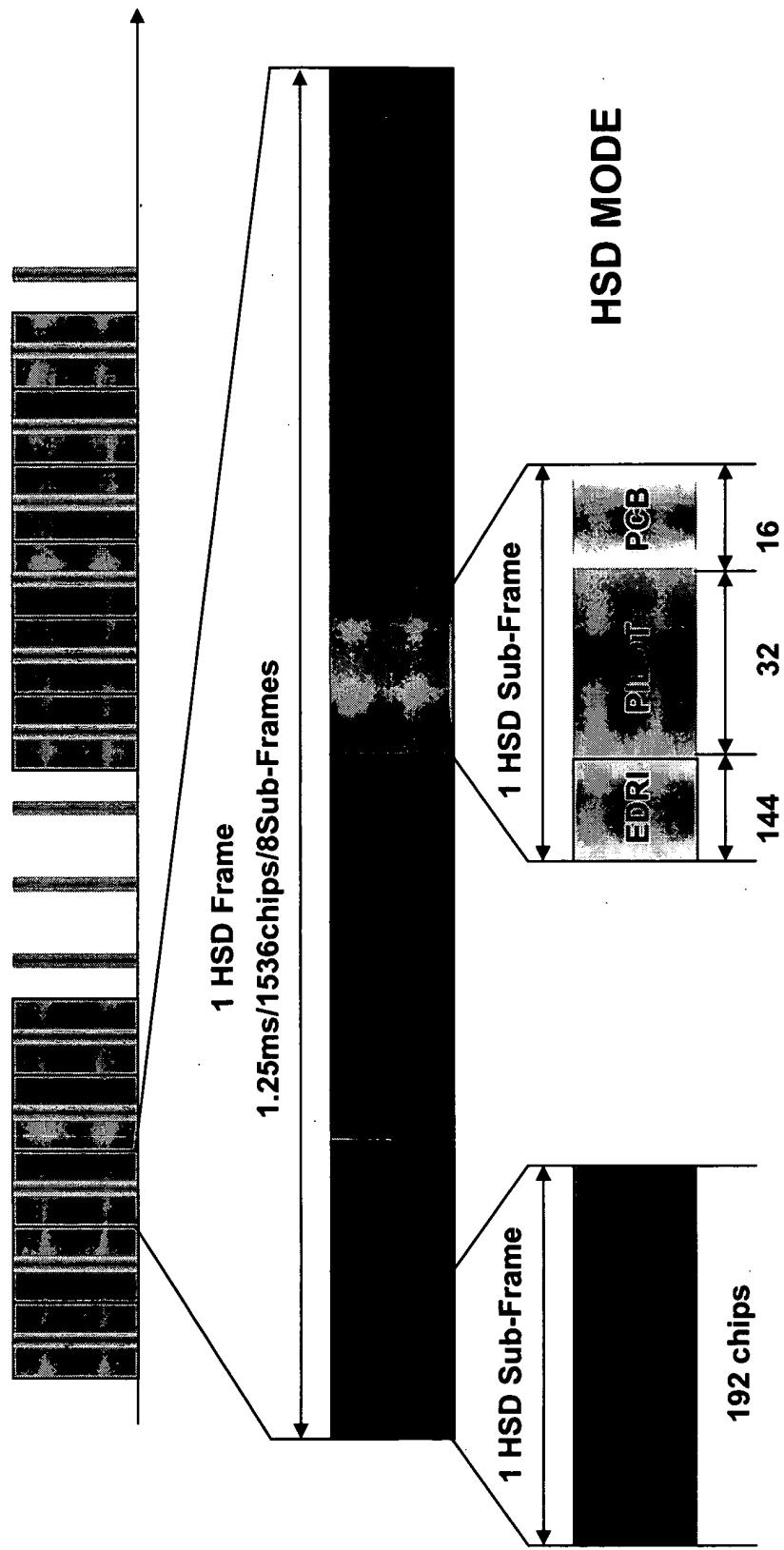
ARQ with Soft-Combining & HOS (WT)

- Coded Symbol Level Packet Detection and Combining
 - Requirements minimum computing and memory for soft combining
 - Simulation Conditions: Packet size:192 bits, AWGN, MAX_re-trans:3, random re-trans position.



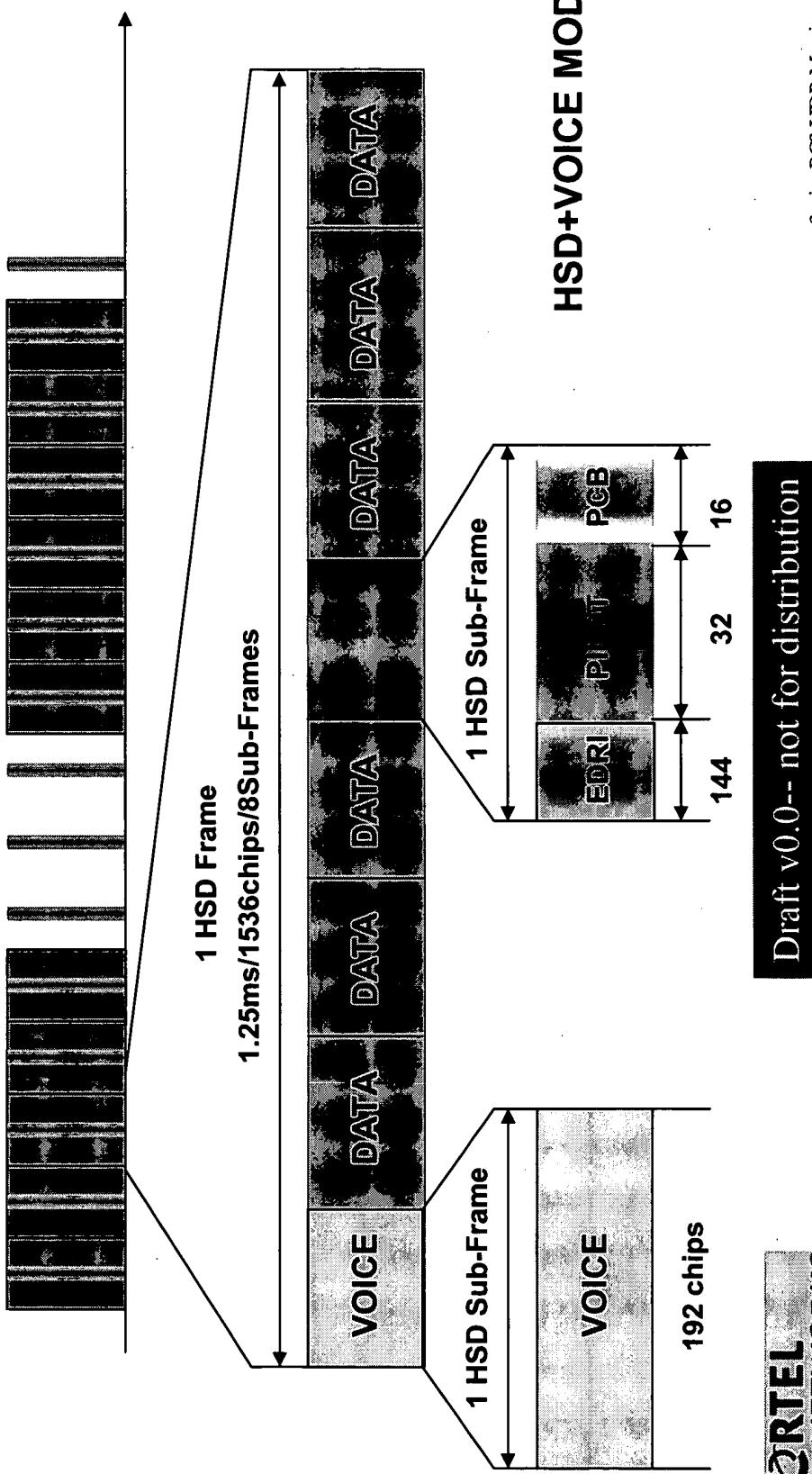
Channel Structure and Control (WT)

- Data Field Blank-&-Burst Transmission
- Pilot/PCB Filled Broadcast Constantly



Channel Structure and Control (WT)

- Provide Voice Service By Using Sub-Frame Stealing
- Maximum 8 HSD+Voice Calls Allowed



Channel Structure and Control (WT)

- 1.25ms Slot Duration aligns RL Power Control Group
- Two Modes User Packet Packing
 - Homogenous Mode: 1-slot only carries one user packets
 - Heterogeneous Mode: 1-slot carries more than one user packets
- Power Control Bit (PCB) Field can Serve 32 users
 - PCB is punctured on I & Q branches separately
- Introduce Explicit Data Rate for Fast Rate Adaptation
 - (12,24) Golay coding is used for Explicit Data Rate Indicator (EDRI), robust to channel error.
 - 4-bit for FL data rates; 5-bit for user Id; 3-bit for heterogeneous mode indication

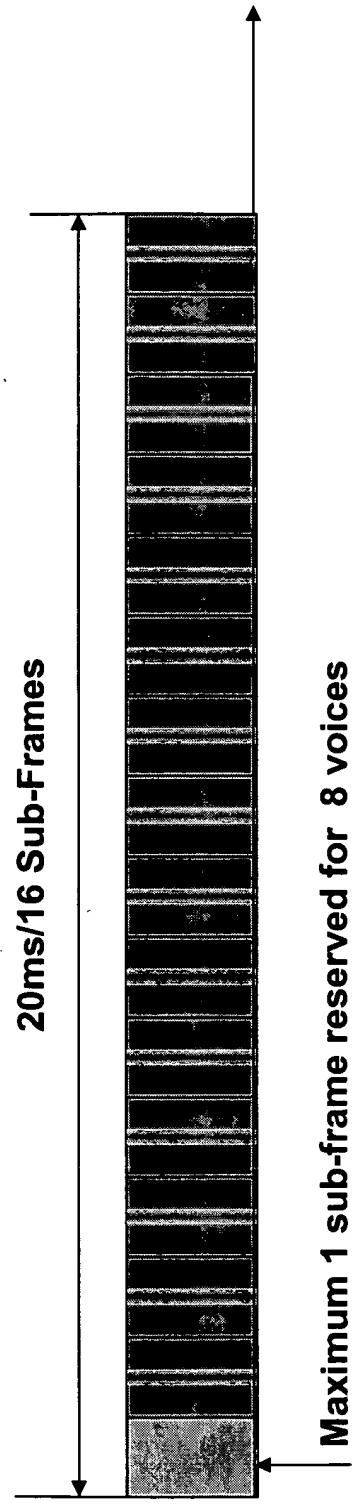
Channel Structure and Control (WT)

- **FL Framing and Control**

- Terminal shall decode the EDRI field and to identify the user Id.
For each HSD frame
 - If the slot Id matches then use the EDRI information to proceed the pilot and data processing
 - The framing is based on the data rate, dynamically allocated to achieve highest throughput per sector
 - One user packet can consist multiple HSD frames
 - In the multicast distribution case, one HSD frame can consist of multi-user packets.
 - In order to support simultaneous voice and data, voice and data can be multiplexed into one HSD frame

Channel Structure and Control (WT)

- **Multiplexing Voice and Data**
 - Provide a support for simultaneous voice and HSD services
 - Maximum 8 voice calls are designed
 - Blind rate detection for voice
- **One Sub-Frame can be Potentially Occupied by Voice**
 - Complete loss for 76.8kpbs rate, 1/16 throughput loss for 1.2288Mbps rate
- **Dynamic Allocate Voice with Flexibility**
 - Gracefully tradeoff peak throughput with voice+HSD service



Physical Channels (WT)

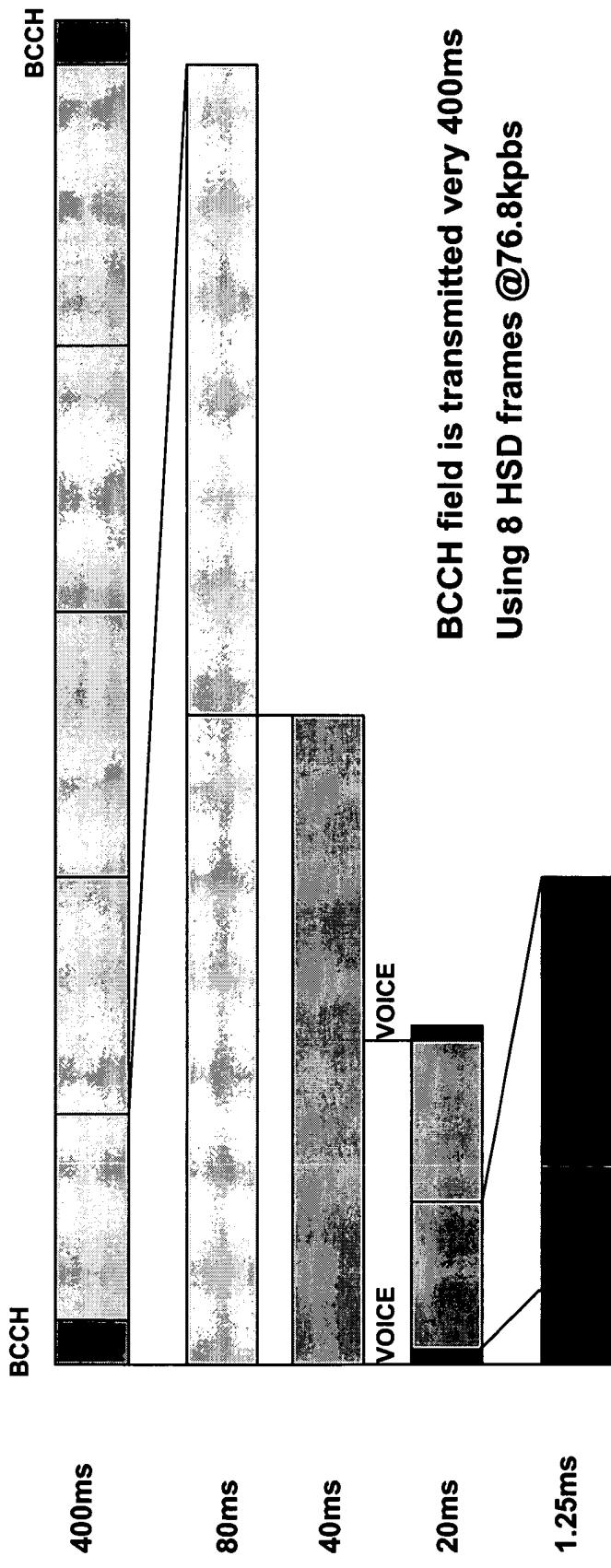
- FL Physical Channel **Consists of 4 Fields in the Slot**
 - Pilot
 - Data
 - EDRI
 - PCB
 - BCCH

- **Design Principles**

- Combine all the FL dedicated traffic channels into data field
- Allocate the common channel into BCCH field
- Create separate power control channel (PCB field)
- Create a set of pilot symbols to allocate:
 - Forward, Tx Diversity, Auxiliary Pilot Channels
- Pack Sync, Paging into BCCH field

Frame Structures: 20/40 ms Super-frame, 2.5 ms Frame (WT)

- 20ms, 40ms and 80ms Super-Frame are used for FL/RL Common Channel Timing
- 2.5ms Frame is used for the RL Fast In-Band Signaling



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Address Bar D:\Space_time_june\Qualcomm\Sprint

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De25		File Folder	2/15/2004 8:12 PM
GengW		File Folder	9/17/2003 11:47 AM
HDR		File Folder	2/12/2005 8:31 AM
JohnHudson		File Folder	9/17/2003 11:47 AM
Moto		File Folder	9/17/2003 11:47 AM
ProvPAT		File Folder	2/15/2004 8:26 PM
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H15T53.xls	42 KB	Microsoft Excel Worksheet	1/7/1999 5:29 AM
HSD Forward Link Peak Rate...	12 KB	Microsoft Word Document	12/14/1999 4:40 PM
HSO.bkp.ppt	467 KB	Microsoft PowerPoint	4/5/1999 10:01 AM
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Object(s) (Disk free space: 1.01 GB)

33.0 MB

My Computer

Exhibit 19

Exh,6,t C

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Invention Disclosure Submission Reply

Disc No:	11962RO	Received Date:	07 jan 2000
Disclosure Title:	High Speed Data Slot Structure		

===== Inventors =====

Global Id	Name	Work Info	Home Info
050355 6	HR Name: TONG, WEN Known As: WEN Email: wentong@nor tel.com Mgr First Name: JOHN Mgr Last Name: HOADLEY Mgr Global ID: 0144683	Location: 3500 CARLING AVENUE OTTAWA ONTARIO K2H 8E9 CANADA Location Code: CAR Dept: DP20 Phone: 3931315 Ext Phone: 7631315 Fax: Ext Fax: MailStop: 04393W26 Citizenship: CANADA	Address: 12 WHITESTONE DRIVE OTTAWA, ON CANADA K2C 4A7 Phone: (0)6137980466
050072 4	HR Name: PERIYALWAR, SHALINI Known As: SHALINI Email: shalinip@nor tel.com Mgr First Name: BILL Mgr Last Name: GAGE Mgr Global ID: 2856986	Location: 3500 CARLING AVENUE OTTAWA ONTARIO K2H 8E9 CANADA Location Code: CAR Dept: AN00 Phone: 3955888 Ext Phone: 613-765-5888 Fax: Ext Fax: MailStop: 04391Y30 Citizenship: CANADA	Address: 88 SUMMERWALK PLACE NEPEAN, ON CANADA K2G 5Y4 Phone: (0)6132742887
150232 4	HR Name: STRAWCZ YNSKI, LEO Known As: LEO Email: Mgr First Name: MAURICE Mgr Last Name: O'SULLIVAN Mgr Global ID: 1402322	Location: 3500 CARLING AVENUE OTTAWA ONTARIO K2H 8E9 CANADA Location Code: CAR Dept: QN10 Phone: Ext Phone: 613-763-7836 Fax: Ext Fax: MailStop: Citizenship: CANADA	Address: 479 HIGHLAND AVENUE OTTAWA, ON CANADA K2A 2J5 Phone: (0)6137299968

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161450 9	HR Name: ROYER, CLAUDE Known As: CLAUDE Email: croyer2@nor tel.com Mgr First Name: ALAUDDIN Mgr Last Name: JAVED Mgr Global ID: 1524826	Location: 3500 CARLING AVENUE OTTAWA ONTARIO K2H 8E9 CANADA Location Code: CAR Dept: DP00 Phone: 3954354 Ext Phone: (613)-765-4354 Fax: Ext Fax: MailStop: 04391W29 Citizenship: CANADA	Address: 170 CHAMPLAIN GATINEAU, QC CANADA J8X 3R2 Phone: (08197766030
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==== Attachments ====

<End of Attachments>

Were there additional inventors involved:	yes	Was there contractor involvement:	no
Name of Supervisor or Divisional Head:		Name of VP:	
CLAUDE ROYER		AL JAVED	
LOB:	Wireless Solutions	Business Unit:	ATCW
Has this invention been discussed with others? If so, please complete:			
Inside Nortel - Whom?		Outside Nortel - Whom?	
Inside Nortel - When?		Outside Nortel - When?	
NDA?	yes		
Are you aware of any imminent future disclosures? Please provide dates and details:			
Yes, Jan 21, 2000 to Sprint PCS (HSD presentation)			
Keywords for Searching:	Products that will use this invention:		
CDMA, High Speed Data, Wireless Internet/IP, Higher Spectral Efficiency Modulation	IS-95/IS-2000 CDMA Networks, 3GPP/UMTS CDMA Networks		
Does this invention arise from any arrangement involving an external organization? no			
Is this invention relevant to a Standards Activity?	Internal Funding Project #'s:		
yes	7417		
If so, give details:			
IS-2000 Release-B in the 3GPP2 Forum 3GPP TSG-RAN WG#1			

Technical Information

Brief Description of the Invention:

An Asymmetric Wireless high speed data system architecture is proposed. The Forward Link is designed as Time Division Multiple Access Mode and the Reverse Link is designed as Code Division Multiple Access Mode. Shorter frame/slot structure is introduced in order to allow the fast coding/modulation and link adaptation on the Forward Link, higher order modulation is used to increase the Forward Link peak data rate.

Problem Solved by the Invention:

The current 3G wireless service has a limited ability to provide Internet service due to the limitation on the data rate. It can not meet the need of fast increase of demand for the higher speed wireless IP service, especially the Internet downloading.

Solutions that have been tried and why they didn't work:

The IS-2000 release-A designed for the 3G wireless service can not deliver higher enough throughput for the Internet access and download. The IS-2000 provides only a limited range of data rate service.

The HSD is designed to enhance the IS-2000 capability for the Wireless Internet applications.

Specific elements or steps that solved the problem and how they do it:

The following are new solutions to enable the high speed data Internet downloading possible:

[1] Short frame and slot structure, to allow

- (1) fast C/I reporting
- (2) fast adaptive coding and modulation
- (3) fast base station selection and re-selection

[2] TDM Blank-and-Burst multiple access scheme to allow

- (1) maximum the data rate for user with best C/I condition
- (2) maximum the high speed rate service range

[3] Puncture bursty pilot and explicit data rate information encoding to allow

- (1) fast and accurate channel estimation
- (2) header decoding to detect the explicit data rate, save terminal power
- (3) maximize the data payload

[4] Slot/bandwidth reservation to allow

- (1) simultaneous voice and data service
- (2) Ensure certain class user QoS

Commercial value of the invention to Nortel and Nortel's major competitors:

A significant market value for the future Nortel wireless and IP business.

Exhibit D

NORTEL NETWORKS LIMITED

POWER OF ATTORNEY

KNOW ALL MEN BY THESE PRESENTS that Nortel Networks Limited (the "Corporation"), a corporation duly organized and existing under the laws of Canada, and having its executive offices at 8200 Dixie Road, Suite 100, Brampton, Ontario, L6T 5P6, Canada, hereby constitutes and appoints Angela deWilton – IP Strategy, Curtis Dodd – IP Counsel, Jean-Pierre Fortin – IP Lead, Jaspreet Harit – IP Counsel and William Junkin – Patent Portfolio Management, as its attorneys, acting individually, with full power and authority to do all acts and things as fully and effectually as and in the name, place and stead of the Corporation, with respect to the prosecution of patent, trademark and copyright applications, the execution of any and all documents respecting the assignment of copyrights held by the Corporation and the release of employees of the Corporation from intellectual property assignment clauses in their employee agreements.

The Corporation hereby ratifies and confirms and agrees to ratify and confirm whatever its said attorneys shall do or purport to do in and by virtue hereof whether prior to or after the date hereof.

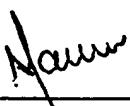
The Power of Attorney issued March 11, 2002, is hereby rescinded without impact or effect on any actions taken thereunder.

IN WITNESS WHEREOF, the Corporation has caused this Power of Attorney to be executed and signed this 14th day of January, 2005, and its corporate seal to be hereunto affixed.

NORTEL NETWORKS LIMITED



Nicholas J. DeRoma
Chief Legal Officer



Gordon A. Davies
Assistant General Counsel – Securities and
Corporate Secretary

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